

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	21	(range near3 value) near5 quartile	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/03/14 12:50
S1	2	"20050071210".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/03/14 09:17
S2	14	710/241.ccls. and weight\$2	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:53
S3	10	intel.as. and (weight\$2 near3 arbit\$6)	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 09:40
S4	724	quartile	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:03
S5	0	710/240-244.ccls. and S4	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:03
S6	148	S4 and arbit\$6	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:04
S7	6	S4 same arbit\$6	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:06
S8	1	S4 same increment\$3 same decrement\$3	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:12
S9	4	S4 with increment\$3	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:12
S10	4655	(one "1") near5 ("75" "76") near3 ("99" "100")	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 11:54
S11	2	(increment\$3 near3 (one "1")) near5 (("75" "76") near2 ("99" "100"))	US-PGPUB; USPAT; EPO; JPO	OR	OFF	2006/03/14 12:50

## WEST Search History

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DATE: Tuesday, March 14, 2006

<b>Hide?</b>	<b>Set Name</b>	<b>Query</b>	<b>Hit Count</b>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L6	L3 and (accumulator near5 (increas\$4 or increment\$4))	2
<input type="checkbox"/>	L5	L3 and accumulator	20
<input type="checkbox"/>	L4	L3 same accumulator	1
<input type="checkbox"/>	L3	quartile	993
<input type="checkbox"/>	L2	L1	416
	<i>DB=USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L1	quartile	416

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L6: Entry 2 of 2

File: USPT

Oct 17, 1995

DOCUMENT-IDENTIFIER: US 5459524 A

TITLE: Phase modulation demodulator apparatus and method

Detailed Description Text (15):

The sample phase 86 and the sample to sample phase increment value 87, as well as the 32 counter output signal 109 are coupled to the reference generator circuit 94. Phase offset value 114 is added to 86 to offset that phase value by an amount suitable for changing the demodulation angle of the reference signals, or alternatively to adjust the tint of the demodulated signal as is common for NTSC demodulators. The phase offset may also be adjusted at 107B as indicated by the dashed connections. Switch 106 is caused to select the adjusted phase value from 113 at the proper time to acquire the newly computed value in response to signal 109. At the next clock that value is incremented by the phase increment value 87 by the phase accumulator comprised of register 98D and adder 107A with the output of 98D coupled through 106 to 107A. Consequently, the incremented phase value from 107A will match each new sample's phase as that sample is present. The phase value for each sample is coupled to the SIN and COS look up tables 111 and 112, via 107B if used, where the proper SIN and COS value for each phase applied is output on 95 and 96. The reference signals 95 and 96 are coupled to the chroma demodulator 90 of FIG. 4, where they are multiplied with the modulated chroma subcarrier in order to demodulate the subcarrier into color difference signals 92 and 93.

Detailed Description Text (22):

As will be realized by one skilled in the art from the present disclosure, several other values which are statistically descriptive of a parameter of the set of samples taken over the burst period may also be used. The set of samples may be just 2, as in the operation of 98e, 98f and 101 of FIG. 5, however it is preferred to use 32 or more such as in FIG. 7. By way of example, any of the various integrals, means, the mode or the median, a quartile, decile, percentile, any of the various deviations, etc. may be suitable for use in particular embodiments which are tailored to achieve specific performance in the presence of specific types of artifacts or degradation. The average has however been found quite preferable for the preferred embodiment, with the integral and median also being envisioned as quite suitable.

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